

Notice of References CitedApplication/Control No.
09/829,136Applicant(s)/Patent Under
Reexamination
ROOHPARVAR, FRANKIE F.Examiner
Cynthia BrittArt Unit
2133

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,550,028	04-2003	Akaogi et al.	714/721
	B	US-6,154,851	11-2000	Sher et al.	714/5
	C	US-5,802,071	09-1998	Fang et al.	714/724
	D	US-5,057,715	10-1991	Larsen et al.	326/121
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"A High-Speed Parallel Sensing Scheme for Multi-level Non-volatile Memories" Calligaro et al. 1997 International Workshop on Memory Technology, Design and Testing Proceedings, pp.: 96-101
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.